



COPY OF PAPERS
ORIGINALLY FILED

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Robert D. Washburn et al.
Application No. : 09/866,563
Issued : May 25th, 2001
Title : **ELECTRONIC ISOLATOR**
Group/Division : 2816
Examiner : Hai L. Nguyen
Our Reference : 0008761-0002

#6/a
9/3/02
JWS

CERTIFICATE OF MAILING (37 C.F.R. § 1.8A)

I hereby certify that this correspondence is, on the date shown below, is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the address shown below.

Date: August 14, 2002


Jose Ramos

Assistant Commissioner for Patents
Washington, D.C. 20231

RESPONSE

Sir/Madam:

In reply to the Office action dated March 15th 2002 (paper # 4), please amend the claims and consider the following remarks:

CLAIMS

We claim:

1. (Amended Once) An electronic isolator between a source stage and a load stage, including at least one circuit path into which electrical noise is directed away from said source and said load stages, including configuring means for configuring said path [isolator] to appear as an infinite impedance to the output signal from said source stage.

2. (Unamended) The electronic isolator of claim 1 wherein said configuring means comprises a voltage source.

RECEIVED
AUG 23 2002
TECHNOLOGY CENT